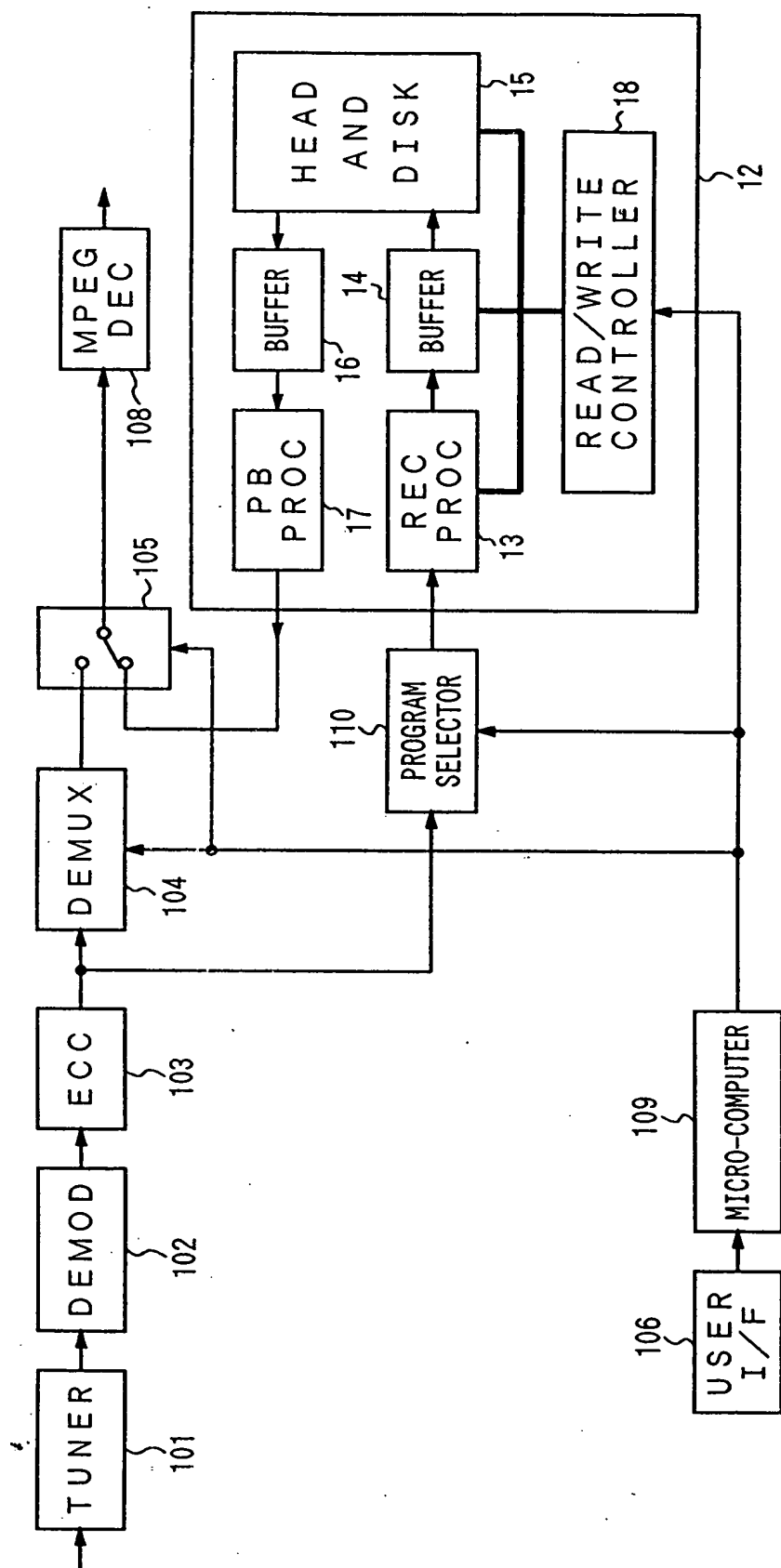


Fig. 1



The diagram illustrates the timing of data transfer between three channels (CH4, CH5, CH6) and a central processor. The horizontal axis represents time, with key events marked by vertical lines: 'pause-on' at T_2 and 'pause-off' at T_3 . The condition $T_3 - T_2 > a_5 - a_4$ is noted.

Data Flow:

- CH4:** Contains segments a_4 , c_4 , d_4 , and b_4 .
- CH5:** Contains segments a_5 , d_5 , and b_5 .
- CH6:** Contains segments a_6 , d_6 , and b_6 .

Timing and Operations:

- A 'write' operation is shown between c_4 and d_4 on CH4, with a duration of 17 minutes.
- A 'read' operation is shown between d_4 and b_5 on CH4, indicated by a dashed arrow.
- A 'write' operation is shown between d_5 and b_5 on CH5, indicated by a solid arrow.

effort" 09602h00

Fig. 3A

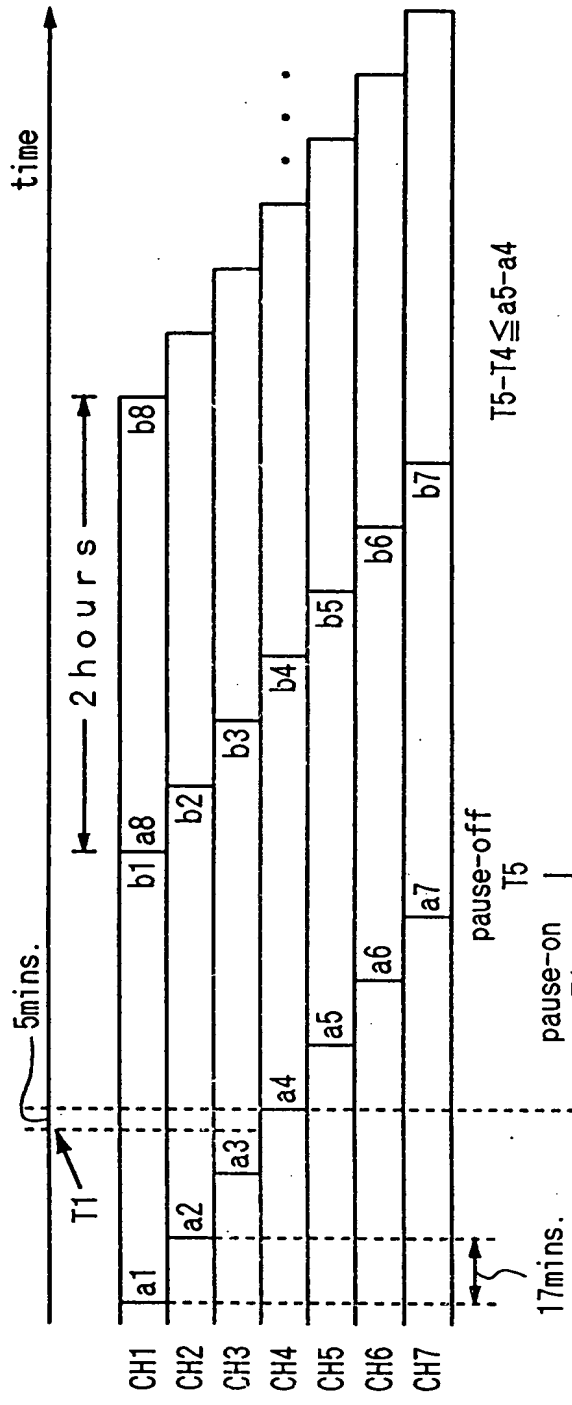
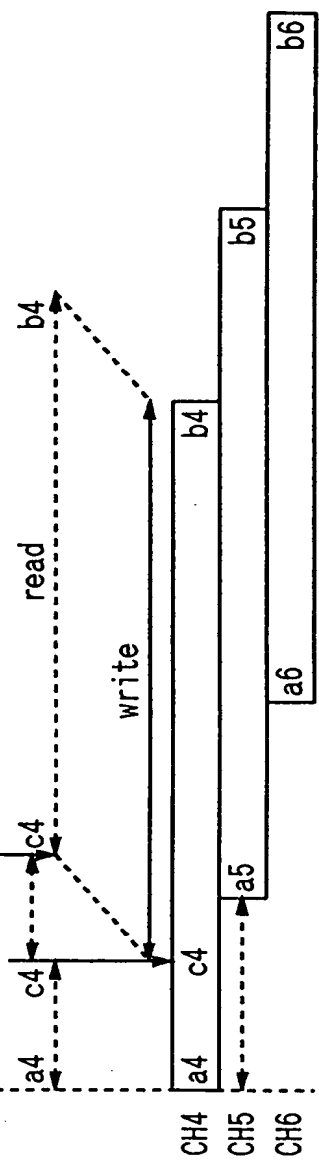


Fig. 3B



66707" 05606460

Fig. 4A

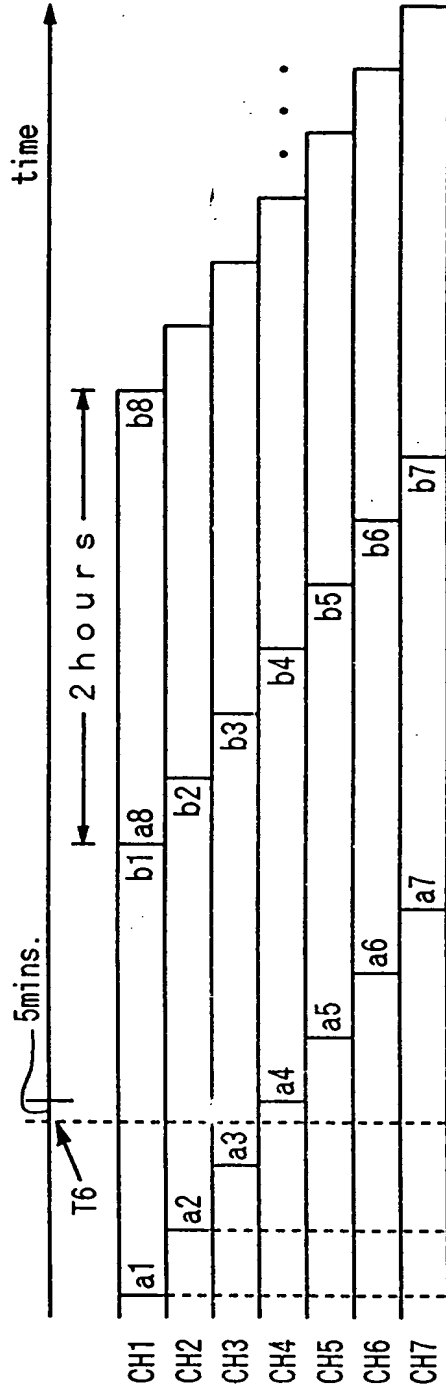
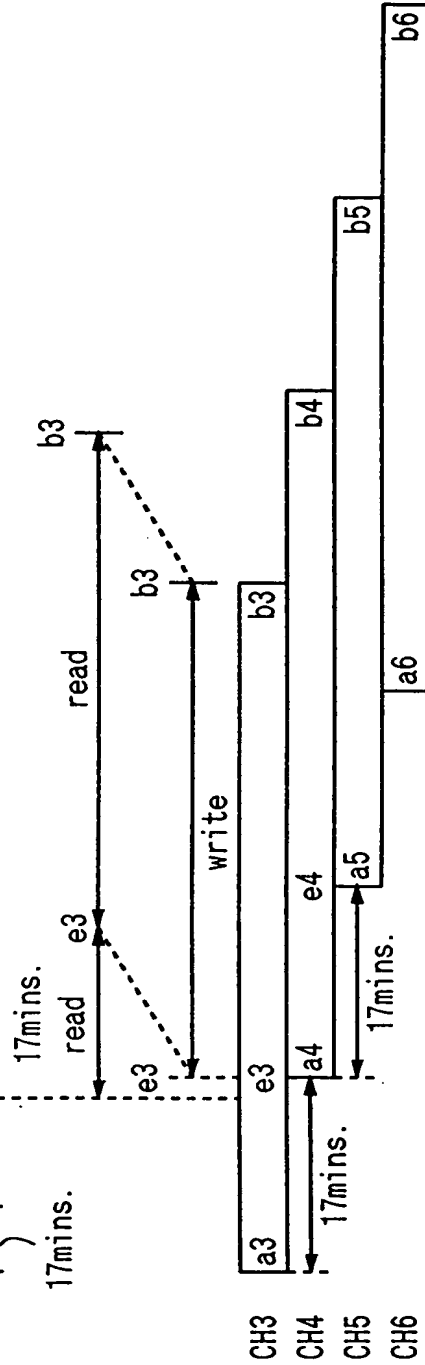
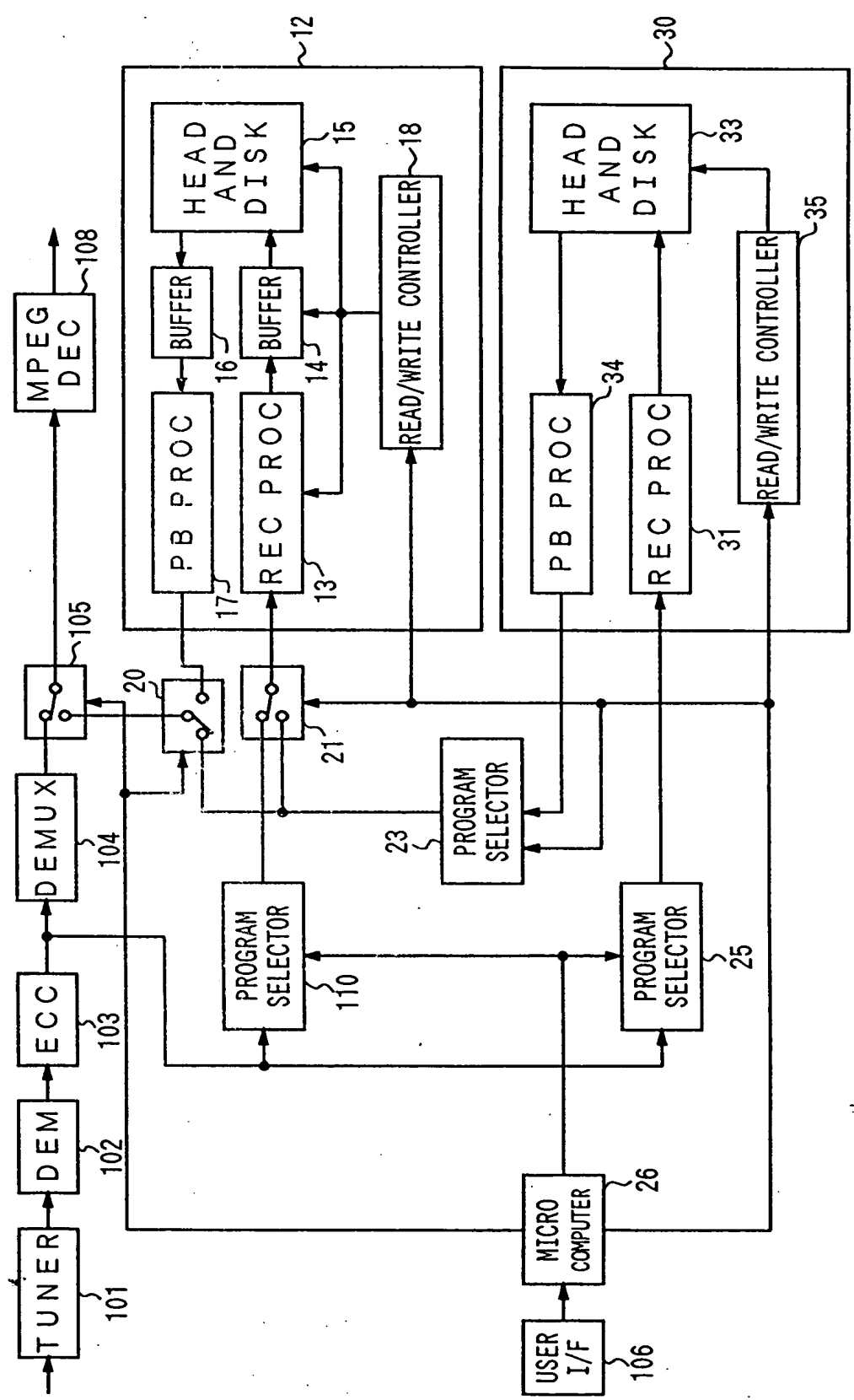


Fig. 4B



66007" 0306E4100

Fig. 5



667077 05602460

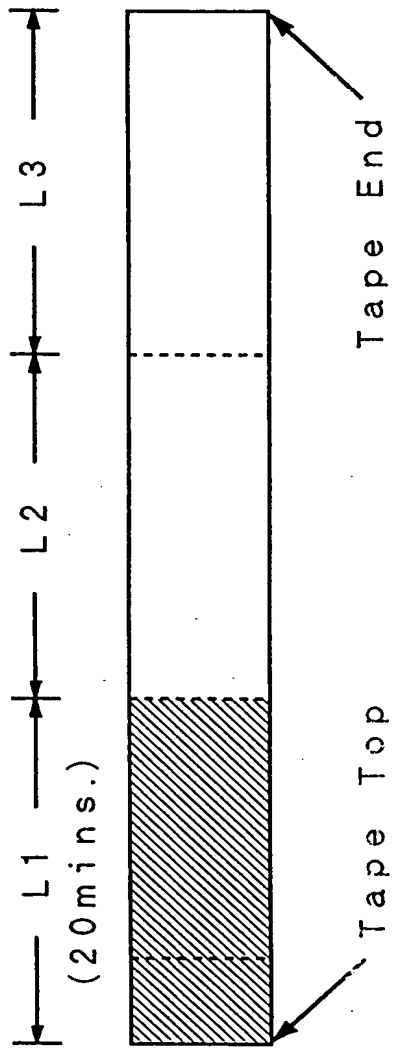
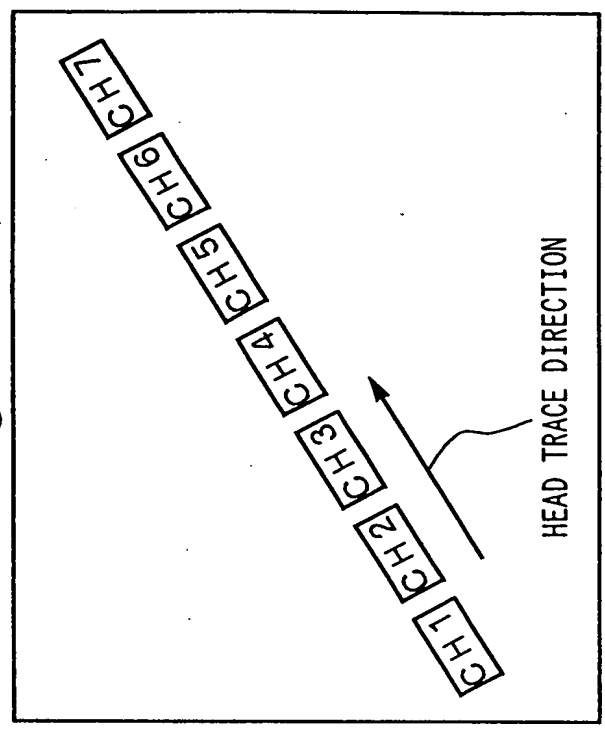


Fig. 6A

Fig. 6C



No. 1 No. 2 No. 3 No. 4 No. 5 No. 6 No. 7	L1	
	L11	L12
	(3mins.) (17mins.)	
	PRV1	PRG1
	PRV2	PRG2
	PRV3	PRG3
	PRV4	PRG4
	PRV5	PRG5
	PRV6	PRG6
	PRV7	PRG7

Fig. 6B

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

Fig. 7

